

PTO/SB/08a (08-03)

Approved for use through 07/31/2008. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
-------	---	----	---

Complete if Known


Application Number	10/725,513
Filing Date	December 3, 2003
First Named Inventor	Farsheed Mahmoudi
Art Unit	2681
Examiner Name	
Attorney Docket Number	15624-US

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	03/29/00
-----------------------	---	--------------------	----------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



PTO/SB/08b (08-03)

Approved for use through 06/30/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/725,513		
		Filing Date	December 3, 2003		
		First Named Inventor	Farsheed Mahmoudi		
		Art Unit	2681		
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	15624-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RC		KIM, Tae Wook et al., "Highly linear RF CMOS amplifier and mixer adopting MOSFET transconductance linearization by multiple gated transistors", IEEE, 2003, pages 107-110.	
RC		KIM, Youngwook et al., "Linearized mixer using predistortion technique", IEEE Microwave and Wireless Components Letters, Vol. 12, No. 6, 2002, pages 204-205.	
RC		Bautista, Edwin E. et al., "A high IIP2 downconversion mixer using dynamic matching", IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, 2000, pages 1934-1941.	
RC		LI, Qiang et al., "CMOS RF mixer no-linearity design", IEEE, 2001, pages 808-811.	
RC		WANG, Zhenhua et al., "A voltage-controllable linear MOS transconductor using bias offset technique", IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, 1990, pages 315-317.	
RC		HEYDARI, Payam, "High-frequency noise in RF active CMOS mixers".	
RC		ROFOUGARAN, A. et al., "A1 GHz CMOS RF front-end IC with wide dynamic range".	

Examiner Signature		Date Considered	3/29/06
-----------------------	--	--------------------	---------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.